**ESE**

**Electrical Engineering**

**ESE 501: System Specification and Modeling**
A comprehensive introduction to the field of System-on-Chip design. Introduces basic concepts of digital system modeling and simulation methodologies. Various types of hardware description language (HDL) will be studied, including Verilog, VHDL, and SystemC. Topics include top-down and bottom-up design methodology, specification language syntax and semantics, RTL, behavioral and system-level modeling, and IP core development. Included are three projects on hardware modeling and simulation.

*Fall, 3 credits, Letter graded (A, A-, B+, etc.)*

**ESE 502: Linear Systems**

*Fall, 3 credits, Letter graded (A, A-, B+, etc.)*

**ESE 503: Stochastic Systems**

*Fall, 3 credits, Letter graded (A, A-, B+, etc.)*

**ESE 504: Performance Evaluation of Communications and Computer Systems**

*Spring, 3 credits, Letter graded (A, A-, B+, etc.)*

**ESE 505: Wireless Communications**
This course covers first year graduate level material in the area of wireless communications: Wireless channels, overview of digital communications and signal processing for wireless commun. voice and data applications, design basics for wireless moderns, analysis of system issues like resource management and handoff, cellular and wireless LAN systems.

*Fall or Spring, 3 credits, Letter graded (A, A-, B+, etc.)*

**ESE 506: Wireless Network**
This course will examine the area of wireless networking and mobile computing, looking at the unique network protocol challenges and opportunities presented by wireless communications and host or router mobility. The course will give a brief overview of fundamental concepts in mobile wireless systems and mobile computing, it will then cover system and standards issues including second generation circuit switched and third generation packet switched networks, wireless LANs, mobile IP, ad-hoc networks, sensor networks, as well as issues associated with small handheld portable devices and new applications that can exploit mobility and location information. This is followed by several topical studies around recent research publications in mobile computing and wireless networking field. This course will make the system architecture and applications accessible to the electrical engineer.

*3 credits, Letter graded (A, A-, B+, etc.)*

**ESE 508: Analytical Foundations of Systems Theory**
An exposition of the basic analytical tools for graduate study in systems, circuits, control, and signal processing. Sets and mappings, finite-dimensional linear spaces, metric spaces, Banach spaces, Hilbert spaces. The theory will be developed and exemplified in the context of systems applications such as nonlinear circuits, infinite networks, feedback control, signal restoration via projections, and optimal signal modeling.

*Spring, 3 credits, Letter graded (A, A-, B+, etc.)*

**ESE 510: Electronic Circuits**
This is a course in the design and analysis of analog circuits, both discrete and integrated. The first part of the course presents basic topics related to circuit analysis: laws, theorems, circuit elements and transforms. Fundamental semiconductor devices are introduced next. A number of aspects of circuit design beginning with basic device operation through the design of large analog functional blocks including amplifiers, oscillators and filters are discussed.

*Cannot be used to fulfill any ESE degree requirements.*

*Fall, 3 credits, Letter graded (A, A-, B+, etc.)*

**ESE 511: Solid-State Electronics**
A study of the electron and hole processes in solids leading to the analysis and design of solid-state electronic devices. Solutions to the Schrodinger representation of quantum effects, perturbation techniques. Simple band structure, effective mass theorem. Derivation and application of the Boltzman transport theory. Electrical and thermal conductivities of metals and of semiconductors, Hall effect, thermal effects, and their application to electronic devices. Properties of semiconductors and the theories underlying the characteristics of semiconductor devices.

*Fall, 3 credits, Letter graded (A, A-, B+, etc.)*

**ESE 512: Bipolar Junction and Heterojunction Electronic Devices**
A study of fundamental properties of homojunction and heterojunction semiconductor devices. Derivation of the characteristic equation for p-n junction diodes, for the bipolar junction transistor (BJT) and for the heterojunction bipolar transistor (HBT); the device parameters for low- and high-frequency operation, the effects on the device characteristics of fabrication methods and of structural arrangements. The development of the large-signal and small-signal equivalent circuits for the p-n diode and the BJT and HPT devices, with emphasis on models used in prevalent computer-aided analysis (e.g., SPICE). Consideration of the devices in integrated-circuit applications.

*Spring, 3 credits, Letter graded (A, A-, B+, etc.)*

**ESE 514: MOS Transistor Modeling**
An overview of the metal-oxide semiconductor (MOS) transistor and its models for circuit analysis. The course is modular in structure. In a common first part, CMOS fabrication, device structure and operation are introduced. Starting from basic concepts of electrostatics, MOS field-effect transistor operation is presented in an intuitive fashion, and no advanced background in solid-state theory is required. Analytical models of increasing complexity and their SPICE Implementations are discussed. The second part of the course allows students to focus on their field of preference: Device physics; digital circuits;
Analog circuits. The course includes a project in one of these subtopics.

**Fall, 3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 515: Quantum Electronics I**

Physics of microwave and optical lasers. Topics include introduction to laser concepts; quantum theory; classical radiation theory; resonance phenomena in two-level systems; Block equations-Kramers-Kronig relation, density matrix; rate equation and amplification; CO₂ lasers; discharge lasers; semiconductor lasers.

**Fall, 3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 516: Integrated Electronic Devices and Circuits I**

Theory and applications: elements of semiconductor electronics, methods of fabrication, bipolar junction transistors, FET, MOS transistors, diodes, capacitors, and resistors. Design techniques for linear digital integrated electronic components and circuits. Discussion of computer-aided design, MSI, and LSI.

**Fall, 3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 517: Integrated Electronic Devices and Circuits II**

Theory and applications: elements of semiconductor electronics, methods of fabrication, bipolar junction transistors, FET, MOS transistors, diodes, capacitors, and resistors. Design techniques for linear digital integrated electronic components and circuits. Discussion of computer-aided design, MSI, and LSI.

**Spring, 3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 518: Advanced design of low noise and low power analog circuits**

Students will learn state-of-the-art circuit techniques for low-noise and low-power amplification and processing of signals from sensors. Examples of circuits are low-noise amplifiers, filters, peak directors and discriminators. Applications range from medical, to security, safety, industrial measurements and physics research. As a course project, students will develop part of a frontend circuit from transistor level to physical layout using industry-standard CAD tools, and will participate in the experimental characterization of those similar circuits. At the end of the course the student will own a solid background and the basic instruments to design low-noise and low-power amplifiers and processing circuits.

**Offered in**

**Fall, 3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 519: Semiconductor Lasers and Photodetectors**

The course provides an introduction to performance, testing and fabrication techniques for semiconductor lasers and photodetectors. The topics include fundamentals of laser and detector operation, devices band diagram, device characteristics, and testing techniques for analog and digital edge emitting and surface emitting lasers, avalanche and PIN photodetectors. Special attention is given to the design and working characteristics of transmitters and pumping lasers for telecommunication networks.

**3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 520: Applied Electromagnetics**


**Spring, 3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 521: Applied Optics**

This course teaches students the fundamental techniques necessary for analyzing and designing optical systems. Topics include matrix methods for ray optics, fundamentals of wave optics, beam optics, Fourier optics and electromagnetic optics. The latter part of the course will deal with optical activity in anisotropic media and include polarization and crystal optics, electro-optics and acousto-optics.

**3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 522: Fiber Optic Systems**

This course covers the essential components of a modern optical fiber communication system: (I) wave propagation in optical fiber waveguides, (II) transmitter design, (III) receiver design, (IV) single wavelength fiber-optic networks, and (V) wavelength division multiplexing networks.

**Prerequisite: ESE 319**

**Fall, 3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 524: Microwave Acoustics**

Continuum acoustic field equations. Wave equation, boundary conditions, and Pointing vector. Waves in isotropic elastic media: plane-wave modes, reflection and refraction phenomena, bulk-acoustic-wave (BAW) waveguides, surface acoustic waves (SAW). Plane and guided waves in piezoelectric media. BAW transduction and applications: delay-line and resonator structures, the Mason equivalent circuit, monolithic crystal filters, IMCON dispersive delay lines, acoustic microscopes, SAW transduction and applications: the interdigital transducer, band-pass filters, dispersive filters, convolvers, tapped delay lines, resonators.

**Prerequisite:** ESE 319

**Fall, 3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 525: Modern Sensors**

Sensors are devices that convert physical values into electrical signals. This course will provide practical information on diversified subjects related to the operation principles, design and use of various sensors. Established and novel sensor technologies as well as problems of interfacing various sensors with electronics are discussed.

**Offered in**

**Spring, 3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 526: Silicon Technology for VLSI**

This course introduces the basic technologies employed to fabricate advanced integrated circuits. These include epitaxy, diffusion, oxidation, chemical vapor deposition, ion implantation lithography and etching. The significance of the variation of these steps is discussed with respect to its effect on device performance. The electrical and geometric design rules are examined together with the integration of these fabrication techniques to reveal the relationship between circuit design and the fabrication process.

**Fall, 3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 527: Circuit Theory and Applications**

Foundation of design procedures for electric circuits. Fundamental concepts, graph theory, network equations, network functions, state equations, network synthesis, scattering parameters, nonlinear circuits.

**Fall, 3 credits, Letter graded (A, A-, B+, etc.)**

**ESE 528: Communication Systems**

This course provides a general overview of communication theory and addresses fundamental concepts in this field. After a review of signals and systems representations, various continuous and digital modulation schemes are analyzed. Spread spectrum systems and their application to multiuser communications are also addressed. Advanced communication systems are described and general concepts of wide and local area networks are introduced.

**Fall, 3 credits, Letter graded (A, A-, B+, etc.)**
ESE 529: Electrical Network Theory
Linear and nonlinear electrical networks; graph theory; determination of operating points; transient estimation; interconnection networks; numerical methods; parameter extraction; infinite and transfinte networks; discrete potential theory; random walks on networks. Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 530: Computer-Aided Design
The course presents techniques for analyzing linear and nonlinear dynamic electronic circuits using the computer. Some of the topics covered include network graph theory, generalized nodal and hybrid analysis, companion modeling, Newton's method in n-dimensions and numerical integration. Prerequisite: B.S. in Electrical Engineering Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 531: Detection and Estimation Theory
Hypothesis testing and parameter estimation. Series representation of random processes. Detection and estimation of known signals in white and nonwhite Gaussian noise. Detection of signals with unknown parameters. Prerequisite: ESE 503 or permission of instructor Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 532: Theory of Digital Communication
Optimum receivers, efficient signaling, comparison classes of signaling schemes. Channel capacity theorem, bounds on optimum system performance, encoding for error reduction, and the fading channel. Source coding and some coding algorithms. Prerequisite: ESE 503 Fall, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 535: Information Theory and Reliable Communications
Measures of information: entropy, relative entropy, and mutual information. The asymptotic equipartition property. Lossless source coding: Kraft inequality and the source coding theorem. Introduction to error correcting codes. Continuous and waveform channels. Rate-distortion theory. Prerequisite: ESE 503 or equivalent or permission of instructor Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 536: Switching and Routing in Parallel and Distributed Systems
This course covers various switching and routing issues in parallel and distributed systems. Topics include message switching techniques, design of interconnection networks, permutation, multicast and all-to-all routing in various networking nonblocking, and rearrangeable capability analysis and performance modeling. 3 credits, Letter graded (A, A-, B+, etc.)

ESE 540: Reliability Theory
Theory of reliability engineering. Mathematical and statistical means of evaluating the reliability of systems of components. Analytical models for systems analysis, lifetime distributions, repairable systems, warranties, preventive maintenance, and inspection. Software reliability and fault tolerant computer systems. 3 credits, Letter graded (A, A-, B+, etc.)

ESE 541: Digital System Design
The course provides an introduction to digital and computer systems. The course follows a top-down approach to presenting design of computer systems, from the architectural-level to the gate-level. VHDL language is used to illustrate the discussed topics. Topics include design hierarchy and top-down design, introduction to hardware description languages, computer-aided design and digital synthesis, basic building blocks like adders, comparators, multipliers, latches, flip-flops, registers etc, static and dynamic random access memory, data and control buses, fundamental techniques for combinational circuit analysis and design, sequential circuit design procedures, and programmable logic devices. Testing of digital designs is addressed throughout the course. A mini project will complement the course. Cannot be used to fulfill any ESE degree requirements. Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 542: Product Design Concept Development and Optimization
This graduate course will concentrate on the design concept development of the product development cycle, from the creative phase of solution development to preliminary concept evaluation and selection. The course will then cover methods for mathematical modeling, computer simulation and optimization. The concept development component of the course will also cover intellectual property and patent issues. The course will not concentrate on the development of any particular class of products, but the focus will be mainly on mechanical and electromechanical devices and systems. As part of the course, each participant will select an appropriate project to practice the application of the material covered in the course and prepare a final report. Prerequisites: Undergraduate electrical or mechanical engineering and/or science training. Fall, 3 credits, Letter graded (A-, A-, B+, etc.)

ESE 544: Network Security Engineering
An introduction to computer network and telecommunication network security engineering. Special emphasis on building security into hardware and hardware working with software. Topics include encryption, public key cryptography, authentication, intrusion detection, digital rights management, firewalls, trusted computing, encrypted computing, intruders and virus. Some projects. Fall, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 545: Computer Architecture
The course covers uniprocessor and pipelined vector processors. Topics include: hierarchical organization of a computer system; processor design; control design; memory organization and virtual memory; I/O systems; balancing subsystem bandwidths; RISC processors; principles of designing pipelined processors; vector processing on pipelines; examples of pipelined processors. The course involves a system design project using VHDL. Prerequisite: ESE 318 or equivalent Spring, 4 credits, Letter graded (A, A-, B+, etc.)

ESE 546: Computer Communications Network
An introduction to the quantitative and qualitative aspects of telecommunication networks. Continuous time and discrete time single queue system analysis. Data link, network, and transport protocols layers. Network interconnection. Multiple access techniques. Flow and congestion control. High speed switching. Prerequisite: ESE 503 or permission of instructor Fall, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 547: Digital Signal Processing
ESE 548: Local & Wide Area Networks
Extended coverage of specific network protocols. Protocols covered include IEEE 802 local area network protocols. Asynchronous Transfer Mode (ATM), Synchronous optical Network (SONET), metropolitan area network protocols, backbone packet switching protocols, and transport control protocol/Internet protocol (TCP/IP), network security, web server design and grid computing.
Prerequisite: ESE 546 or permission of instructor
Summer, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 549: Advanced VLSI System Testing
This course is designed to acquaint students with fault diagnosis of logic circuits. Both combinatorial and sequential circuits are considered. Concepts of faults and fault models are presented. Emphasis is given to test generation, test selection, fault detection, fault location, fault location within a module and fault correction.
Prerequisite: BS in Electrical Engineering
Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 550: Network Management and Planning
This course provides an introduction to telecommunications and computer network management and planning. Network management is concerned with the operation of networks while network planning is concerned with the proper evolution of network installations over time. Network management topics include meeting service requirements, management operations, management interoperability, and specific architectures such as Telecommunications Management Network (TMN), and Simple Network Management Protocol (SNMP). Network planning topics include planning problem modeling, topological planning design, heuristic and formal solution techniques.
Fall, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 551: Switching Theory and Sequential Machines
Survey of classical analysis and synthesis of combination and sequential switching circuits, followed by related topics of current interest such as error diagnosis and fault soft circuits, use of large-scale integration, logic arrays, automated local design.
Prerequisite: ESE 318 or equivalent
Fall, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 552: Interconnection Networks
Formation and analysis of interconnect processing elements in parallel computing organization. Topics include: SIMD/MIMD computers, multiprocessors, multicomputers, density, symmetry, representations, and routing algorithms. Topologies being discussed include: Benes, Omega, Banyan, mesh, hypercube, cube-connected cycles, generalized chordal rings, chordal rings, DeBrujin, Moebius graphs, Cayley graphs, and Borel Cayley graphs.
Prerequisite: ESE 545 or equivalent
Fall, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 553: A/D and D/A Integrated Data Converters
This is an advanced course on analog integrated circuit design aspects for data converters. Topics include: continuous and discrete-time signals and systems; sampling theorem; ideal ND and D/A converters; specifications and testing of data converters; basic building blocks in data converters: current sources and mirrors, differential gain stages, voltage references, S/H circuits, comparators: Nyquist D/A and ND converters; principles of data conversion and circuit design techniques; oversampling data converters: low-pass and band-pass delta-sigma modulators, decimation and interpolation for delta-sigma data converters. The attending students must be acquainted with principles of transistor operation, function of simple analysis. Familiarity with SPICE is required.
3 credits, Letter graded (A, A-, B+, etc.)

ESE 554: Computational Models for Computer Engineers
This course covers mathematical techniques and models used in the solution of computer engineering problems. The course heavily emphasizes computer engineering application. Topics covered include set theory, relations, functions, graph theory and graph algorithms, and algebraic structures.
Fall, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 555: Advanced VLSI Systems Design
Techniques of VLSI circuit design in the MOS technology are presented. Topics include MOS transistor theory, CMOS processing technology, MOS digital circuit analysis and design, and various CMOS circuit design techniques. Digital systems are designed and simulated throughout the course using an assortment of VLSI design tools.
Prerequisite: B.S. in Electrical Engineering or Computer Science
Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 556: VLSI Physical and Logic Design Automation
Areas to be covered are Physical Design Automation and Logic Design Automation. Upon completion of this course, students will be able to design logic and design tools and algorithms for VLSI logic and physical design. Tools will address design tasks such as floor planning, module placement and signal routing. Also, automated optimization of combinational and sequential circuits will be contemplated.
Prerequisite: B.S. in Computer Engineering/Science or Electrical Engineering
Fall, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 557: Digital Signal Processing II: Advanced Topics
A number of different topics in digital signal processing will be covered, depending on class and current research interest. Areas to be covered include the following: parametric signal modeling, spectral estimation, multirate processing, advanced FFT and convolution algorithms, adaptive signal processing, multidimensional signal processing, advanced filter design, dedicated signal processing chips, and signal processing for inverse problems. Students will be expected to read and present current research literature.
Prerequisite: ESE 547 or permission of instructor
Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 558: Digital Image Processing I
Covers digital image fundamentals, mathematical preliminaries of two-dimensional systems, image transforms, human perception, color basics, sampling and quantization, compression techniques, image enhancement, image restoration, image reconstruction from projections, and binary image processing.
Prerequisite: B.S. in Engineering or Physical or Mathematical Sciences
Fall, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 559: Digital Image Processing II
The course material will proceed directly from DIP-I, starting with image reconstruction from projections. After the basic projection, theorems are developed and computerized axial tomography techniques will be examined in detail including forward and inverse random transformations, convolution, back projection, and Fourier reconstruction; nuclear magnetic resonance imaging and positron emission tomography will be similarly covered. Surer resolution concepts will be developed.
and applied to a variety of remote sensing applications as well as digital image coding for efficient transmission of digital TV imagery.

Prerequisite: ESE 558

Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 568: Computer and Robot Vision

Principles and applications of computer and robot vision are covered. Primary emphasis is on techniques and algorithms for 3D machine vision. The topics include image sensing of 3D scenes, a review of 2D techniques, image segmentation, stereo vision, optical flow, time-varying image analysis, shape from shading, texture, depth from defocus, matching, object recognition, shape representation, interpretation of line drawings, and representation and analysis of 3D range data. The course includes programming projects on industrial applications of robot vision.

3 credits, Letter graded (A, A-, B+, etc.)

ESE 570: Bioelectronics

Origin of bioelectric events; ion transport in cells; membrane potentials; neural action potentials and muscular activity; cortical and cardiac potentials. Detection and measurement of bioelectric signals; impedance measurements used to detect endocrine activity, perspiration, and blood flow; impedance cardiography; vector cardiography; characteristics of transducers and tissue interface; special requirements for the amplification of transducer signals.

Fall, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 571: Introduction to Auto ID Technologies

Introductory course is a series of Auto ID systems, technologies and applications. The course covers theory and applications of important data-capture technologies, namely, barcodes, biometrics and RFID. Topics to be covered include: architecture of data-capture/ Auto ID system, barcodes: overview of 1-D and 2-D barcodes and other LOS technologies; biometrics: fingerprints, iris-scan, voice recognition and smart-cards; radio frequency identification (RFID): fundamentals, near-field vs. far-field, UHF read range estimation, reader sensitivity limits, tag singulation and multiple access protocols, standards, privacy and security issues in RFID, real time location systems (RTLS), and wireless sensor networks. Prerequisites: ESE 372, ESE 218, ESE 305 (ESE 319)

3 credits, Letter graded (A, A-, B+, etc.)

ESE 575: Advanced VLSI Signal Processing Architecture

This course is concerned with advanced aspects of VLSI architecture in digital signal processing and wireless communications. The first phase of the course covers the derivation of both data transformation and control sequencing from a behavioral description of an algorithm. The next phase reviews the general purpose and dedicated processor for signal processing algorithms. This course focuses on low-complexity high-performance algorithm development and evaluation, system architecture modeling, power-performance tradeoff analysis. The emphasis is on the development of application-specific VLSI architectures for current and future generation of wireless digital communication systems. An experimental/research project is required.

3 credits, Letter graded (A, A-, B+, etc.)

ESE 580: Microprocessor-Based Systems Engineering I

This course is a study of methodologies and techniques for the engineering design of microprocessor-based systems. Emphasis is placed on the design of reliable industrial quality systems. Diagnostic features are included in these designs. Steps in the design cycle are considered. Specifically, requirement definitions, systematic design implementation, testing, debugging, documentation, and maintenance are covered. Laboratory demonstrations of design techniques are included in this course. The students also obtain laboratory experience in the use of microprocessors, the development of systems, circuit emulation, and the use of signature and logic analyzers.

Fall, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 581: Microprocessor-Based Systems Engineering II

This course is a study of methodologies and techniques for the engineering design of microprocessor-based systems. Emphasis is placed on the design of reliable industrial quality systems. Diagnostic features are included in these designs. Steps in the design cycle are considered. Specifically, requirement definitions, systematic design implementation, testing, debugging, documentation, and maintenance are covered. Laboratory demonstrations of design techniques are included in this course. The students also obtain laboratory experience in the use of microprocessors, the development of systems, circuit emulation, and the use of signature and logic analyzers.

Spring, 4 credits, Letter graded (A, A-, B+, etc.)

ESE 585: Nanoscale Integrated Circuit Design

This course is a study of methodologies and techniques for the engineering design of microprocessor-based systems. Emphasis is placed on the design of reliable industrial quality systems. Diagnostic features are included in these designs. Steps in the design cycle are considered. Specifically, requirement definitions, systematic design implementation, testing, debugging, documentation, and maintenance are covered. Laboratory demonstrations of design techniques are included in this course. The students also obtain laboratory experience in the use of microprocessors, the development of systems, circuit emulation, and the use of signature and logic analyzers.
This course describes high performance and low power integrated circuit (IC) design issues for advanced nanoscale technologies. After a brief review of VLSI design methodologies and current IC trends, fundamental challenges related to the conventional CMOS technologies are described. The shift from logic-centric to interconnect-centric design is emphasized. Primary aspects of an interconnect-centric design flow are described in four phases: (1) general characteristics of on-chip interconnects, (2) on-chip interconnects for data signals, (3) on-chip power generation and distribution, and (4) on-chip clock generation and distribution. Existing design challenges faced by IC industry are investigated for each phase. Tradeoffs among various design criteria such as speed-power-noise-area are highlighted. In the last phase of the course, several post-CMOS devices, emerging circuit styles, and architectures are briefly discussed. At the end of the course, the students will have a thorough understanding of the primary circuit and physical level design challenges with application to industrial IC design.

Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 588: Pattern Recognition

Basic concepts of pattern recognition techniques are introduced, including statistical pattern recognition, syntactic pattern recognition, and graph matching. Topics on Bayes decision theory, parametric and nonparametric techniques, clustering techniques, formal languages, parsing algorithms, and graph-matching algorithms are covered.

Prerequisite: Stochastic processes and data structures

Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 591: Industrial Project in OEMS Engineering

A student carries out a detailed design of an industrial project in OEMS engineering. A comprehensive technical report of the project and an oral presentation are required.

Fall, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 597: Practicum in Engineering - Internship

This course is for part-time and full-time graduate students, relating to their current professional activity. Participation is in private corporations, public agencies or non-profit institutions. Students will be required to have a faculty advisor as well as a contact in the outside organization to participate with them in regular consultations on their project. Students are required to submit a final written final report to both.

Prerequisite: Must be advanced to candidacy (G5). Major portion of research will take place off-campus, but in the United States and/or U.S. provinces. Please note, Brookhaven National Labs and the Cold Spring Harbor Lab are considered on-campus. All international students must enroll in one of the graduate student insurance plans and should be advised by an International Advisor.

Fall, 1-3 credits, S/U grading

May be repeated for credit.

ESE 599: Research Master's students

Fall and Spring, 1-12 credits, S/U grading

May be repeated for credit.

ESE 610: Seminar in Solid-State Electronics

Current research in solid-state devices and circuits and computer-aided network design. Fall and Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 670: Topics in Electrical Sciences

Varying topics selected from current research topics. This course is designed to give the necessary flexibility to students and faculty to introduce new material into the curriculum before it has attracted sufficient interest to be made part of the regular course material. Topics include biomedical engineering, circuit theory, controls, electronics circuits, digital systems and electronics, switching theory and sequential machines, digital signal processing, digital communications, computer architecture, networks, systems theory, solid-state electronics, integrated electronics, quantum electronics and lasers, communication theory, wave propagation, integrated optics, optical communications and information processing, instrumentation, and VLSI computer design and processing.

Fall and Spring, 3 credits, Letter graded (A, A-, B+, etc.)

May be repeated for credit.

ESE 691: Seminar in Electrical Engineering

This course is designed to expose students to the broadest possible range of the current activities in electrical engineering. Speakers from both on and off campus discuss topics of current interest in electrical engineering.

Fall and Spring, 1 credit, S/U grading

May be repeated for credit.

ESE 697: Ph.D. Practicum in Teaching

This course provides hands-on experience in classroom teaching. Other activities may include preparation and supervision of laboratory experiments, exams, homework assignments and projects. Final report that summarizes the activities and provides a description of the gained experience and a list of recommendations is required.

Prerequisite: G5 status and Permission of Graduate Program Director.

Fall and Spring, 3 credits, Letter graded (A, A-, B+, etc.)

ESE 698: Practicum in Teaching

This course enables graduate students to gain experience in teaching and interacting with students enrolled in an electrical and computer engineering course. Students enrolled in ESE-698 are expected to perform various teaching duties required by the course instructor, such as attending lectures, providing office hours, holding review/recitation sessions, assisting in lab sections and grading, etc.

Fall, 1-3 credits, Letter graded (A, A-, B+, etc.)

May be repeated for credit.

ESE 699: Dissertation Research on Campus

Prerequisite: Advancement to candidacy (G5). Major portion of research must take place on SBU campus, at Cold Spring Harbor, or at the Brookhaven National Lab.

Fall, 1-9 credits, S/U grading

May be repeated for credit.

ESE 700: Dissertation Research off Campus - Domestic

Prerequisite: Must be advanced to candidacy (G5). Major portion of research will take place off-campus, but in the United States and/or U.S. provinces. Please note, Brookhaven National Labs and the Cold Spring Harbor Lab are considered on-campus. All international students must enroll in one of the graduate student insurance plans and should be advised by an International Advisor.

Fall, 1-9 credits, S/U grading

May be repeated for credit.

ESE 701: Dissertation Research off Campus - International

Prerequisite: Must be advanced to candidacy (G5). Major portion of research will take place outside of the United States and/or U.S. provinces. Domestic students have the option of the health plan and may also enroll in MEDEX. International students who are in their home country are not covered by mandatory health plan and must contact the Insurance Office for the insurance charge to be removed. International students who are not in their home country are charged for the mandatory health insurance. If they are to be covered by another insurance plan they must file a waiver be second week of classes. The charge will only be removed if other plan is deemed comparable.
All international students must receive clearance from an International Advisor. Fall, 1-9 credits, S/U grading. May be repeated for credit.

**ESE 800: FULL TIME SUMMER RESEARCH**

May be repeated for credit.